## **Electron Annealing in Aluminum Interconnects Observed Using In Situ TEM Technique**

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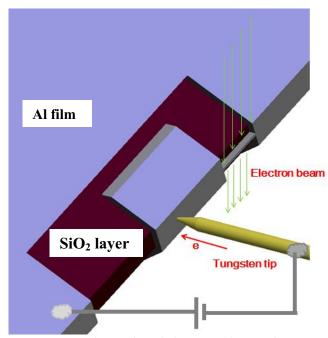
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Interconnect is a thin wire of copper or aluminum alloy which makes electrical contact between devices, typically on Si substrate. Electro-migration is the phenomenon that metallic atoms are transported by electron wind due to high electrical current density in the metal line. Ever since Blech [1] reported a relationship between interconnect length and the rate of electromigration; induced drift, other factors like crystallographic texture [2], grain size and its distribution [3] and grain boundary structures [4] have been extensively documented to have a major impact on electron-migration induced plasticity. The continuous scaling down in the dimensions of typical integrated circuits leads to increasing electric current density in interconnects lines. Consequently, understanding the exact mechanism and evolution of electrical current effect on interconnect has become critical than ever for designing reliable devices.

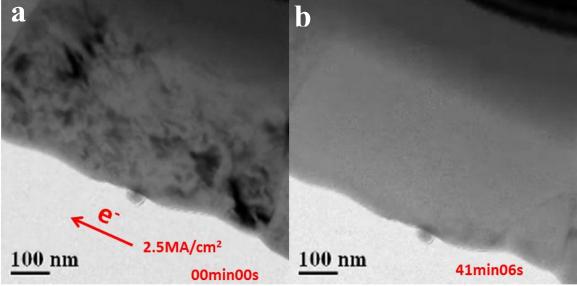
When interconnects' dimension shrinks into submiro- and even nano-meter range, it will become electron transparent inside transmission electron microscope. Consequently, in situ TEM wil become the best tool for studying the microstructure evolution of interconnects under the effect of electrical current. In this work, we developed a novel method for preparing samples that can be studied in situ inside a TEM under the effect of electrical current. **Figure 1** is the schematic for the sample preparation. By employing a Hysitron PI95 ECR type holder, we found that the defects density inside the Al grains can be reduced dramatically by the applied current (**Figure 2**) which we termed as electron annealing [5]. This phenomenon is possible to be used to heal those defected material in integrated circuits and in MEMS devices.

## References

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**Figure 1.** Schematic of the experimental setup. The sample was made from a boron doped silicon wedge with  $SiO_2$  layer and Al coating. FIB was used to pattern a peninsula-like sample at the top of the silicon wedge. High-density current was applied by touching the peninsula with a movable Tungsten tip and applying a electric bias between the "peninsula head" and the "mainland".



**Figure 2.** Bright field TEM images of a Al grain before (a) and after (b) the applied electrical current. The defects density decreased dramatically after the treatment with electron current, a phenomena we have termed as electrical annealing.