

Analysis of Nano-scale Strain Near Shallow Trench Isolation Structures by Energy-filtered Convergent Beam Electron Diffraction

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Shallow trench isolation (STI) is used in silicon technology for electrical isolation between devices. However, stress near the oxide-filled trenches may lead to defect generation and may affect device performance. A crucial task for the development of sub-100nm device technologies is to measure strain at nanometer spatial resolution and control it at nanoscale. Although the convergent beam electron diffraction (CBED) technique of transmission electron microscopy (TEM) has been successfully applied to measure highly localized strain around STI structures [1-3] where the inter-trench silicon islands have sizes larger than 200nm, it is not clear whether CBED can be used to characterize local strain beyond the 90nm technology node.

In this work we studied STI structures used for 90nm and 130 nm flash memory device fabrication. To evaluate the strain-detection sensitivity of CBED, after identical trench lithography, different high density plasma (HDP) steps were used for trench oxide filling. The Zeiss Libra[®] 200kV TEM used in this study features a monochromator, an in-column OMEGA energy filter, and a high-angle annular dark-field detector, which make it ideal for spot-to-spot CBED measurement [4]. By using a 20eV energy selection slit, most plasmon peaks are excluded for CBED pattern formation. The χ^2 fitting [5] of lattice parameters is implemented by the Automatic Strain Analysis by CBED (ASAC) package of Soft Imaging System. Use of the automated procedure has been discussed by Armigliato *et al* [1, 2]. We have used the silicon [340] zone axis for these experiments.

An example of the cross-sectional TEM micrograph of the structures and the CBED patterns collected from sample#1 is presented in Fig.1. As we can see from Fig.1(a)-(c), the HOLZ lines either split or become blurred in the small silicon island. The pattern quality is much better in the adjacent larger island as shown in Fig.1(d)-(f). Lattice parameters could be determined only from Fig.1(a),(d), and (e) which have acceptable quality. The fitted lattice parameters and χ^2 values are listed in Table 1. It appears that the nitride cap provides tensile stress and partially compensates the compression from oxide fillings near the pad oxide.

For two other samples (#2 and #3) prepared with modified HDP recipes, we could get sufficiently sharp HOLZ patterns only near the top of the larger silicon island. However, at this position, one could not isolate the contribution from the isolation nitride. So, we collected patterns 15nm off the bottom of the trench (corresponding to point 7 in Fig.1). For these two samples, it is difficult to tell the difference in strain from only one CBED measurement per sample due to significant noise. For accurate statistics, over ten patterns were collected at the identical position for different trenches in the same TEM sample. The averaged lattice parameters and standard deviation values are given in Table 2. It is found that the compressive strain in sample #2 is larger than in #3.

The application of the CBED technique is limited by at least two characteristics which generate strain gradient along the path of electron beam and blur HOLZ lines: (a) a spontaneous elastic strain relaxation and local lattice distortion from thinning the TEM sample to electron-transparency, and (b) HOLZ lines are usually collected along low-symmetry axes [1, 2] which are several degrees off the [110] axis to minimize dynamical scattering effects. The blurring of CBED patterns is more pronounced in smaller devices due to higher stress gradients in such devices. In our presentation, we will discuss possible sample preparation techniques to minimize this effect.

References:

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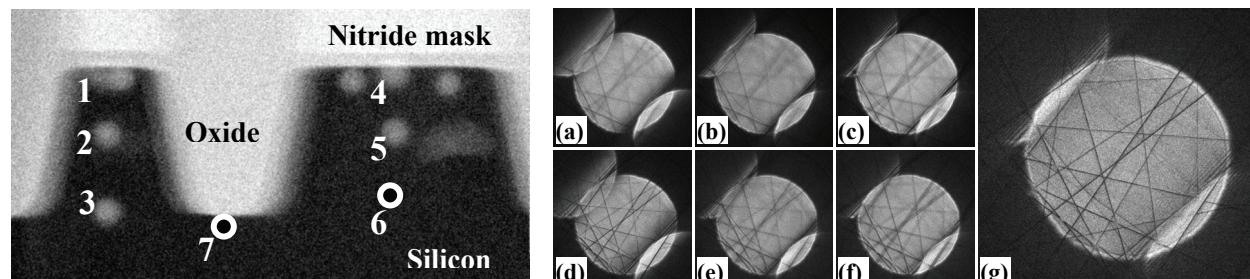


Fig. 1. On the left: Dark field STEM image of the STI structures taken along Si [340] zone axis. Contamination spots (white dots) were left on the sample after excessive e-beam exposure. On the right: CBED patterns (a)-(f) taken at points 1-6 depicted on the TEM image on the left, respectively. (g) was taken from bulk silicon. No digital processing has been applied to these patterns.

Table 1. Lattice parameters measured at different positions (a), (d), and (e) of sample#1.

Pattern	a (Å)	b (Å)	c (Å)	α (degree)	β (degree)	γ (degree)	χ^2	Trace of strain tensor ($\times 10^{-4}$)
(a)	5.4303	5.4303	5.4312	89.98	90.02	89.98	1.0396	-2.2
(d)	5.4298	5.4298	5.4344	90.01	89.99	89.99	0.4907	1.8
(e)	5.4277	5.4277	5.4301	89.97	90.03	89.95	1.0243	-13.8

Table 2. Lattice parameters measured at 15 nm off the bottom of oxide filling for sample #2 and #3.

Sample	a (Å)	b (Å)	c (Å)	α (degree)	β (degree)	γ (degree)	Trace of strain tensor ($\times 10^{-4}$)
#2	5.4310 ±0.0003	5.4310 ±0.0003	5.4283 ±0.0007	90.00 ±0.01	90.00 ±0.01	90.00 ±0.02	-4.9±1.3
#3	5.4312 ±0.0007	5.4312 ±0.0007	5.4299 ±0.0010	90.00 ±0.02	90.00 ±0.02	90.00 ±0.01	-1.3±3.2